

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Steven L. Scott  
Title: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR  
COMPUTER

Docket No.: 1376.700US1  
Filed: August 18, 2003  
Examiner: Sheng Jen Tsai

Serial No.: 10/643,585  
Due Date: March 25, 2007  
Group Art Unit: 2186

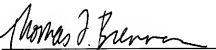
MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Amendment and Response Under 37 CFR 1.116 (16 pgs.).  
☒ Replacement Figs. 1-10 and new Figs. 11-12 (7 sheets)

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

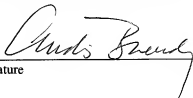
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number 21186

By:   
Atty: Thomas F. Brennan  
Reg. No. 35,075

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22 day of March, 2007.

CANDIS BUENDING

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(GENERAL)